



U.S. Patent Application No.: 10/716,595
Attorney Docket No.: 57941.000062
Client Reference No.: RA001.2003.1.C.US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: :
Michael FARMWALD et al. : Group Art Unit: 2827
Appln. No.: 10/716,595 : Examiner: Tan Nguyen
Filed: November 20, 2003 : Confirmation No.: 7213
For: MEMORY DEVICE AND METHOD FOR : Customer No.: 38013
OPERATING SAME :

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with the duty under 37 C.F.R. § 1.56 of each individual associated with the filing and prosecution of the above-identified patent application (hereinafter, "associated individuals") to disclose all information known to that individual to be material to patentability, Applicant(s) hereby submits attached Form PTO-1449 (modified) listing cited references. This submission is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609.

Please note that the vast majority of the cited references were recently identified by parties contesting the validity of U.S. patents related to above-identified patent application in two separate proceedings before the U.S. District Court for the

Northern District of California, namely Rambus Inc. v. Samsung Electronics Co., Ltd. et al. (Case No. C 05-02298 RMW) and Hynix Semiconductor Inc. et al. v. Rambus Inc. (Case No. CV 00-20905 RMW). To avoid overburdening the Examiner as much as possible, Applicants have checked to make sure that the cited references have not been previously cited in the above-identified patent application.

The cited references, while believed to be of some relevance, are not necessarily considered to teach or suggest any aspect of the invention described and claimed in the above-identified patent application. Applicant(s) hereby expressly reserves the right to swear behind the effective dates of any of the cited references. Applicant(s) further reserves the right to question the relevance, materiality, and/or prior art status of any of the cited references in whole, in part, or in combination, subsequent to the filing of this information disclosure statement. This information disclosure statement is also not to be construed as a representation that a search has, or has not, been conducted or that no better art exists. Rather, this information disclosure statement discloses only the best references of which the associated individuals are aware.

The Examiner is respectfully requested to consider each of the cited references, to indicate such consideration by

initialing in the space provided next to each cited reference on the enclosed Form PTO-1449 (modified), to sign the initialed Form PTO-1449 (modified), and to return a copy of the same with the next communication to the Applicant(s).

In accordance with 37 C.F.R. § 1.98(a), only copies of the cited references which are not U.S. patents or U.S. patent application publications (if any) are being submitted herewith. However, copies of the cited references which are U.S. patents or U.S. patent application publications will be submitted at the request of the Examiner. In considering these cited references, it may be noted by the Examiner that certain of the references may contain markings, underlinings, and/or other notations. These markings, underlinings, and/or other notations are not to be construed as drawing the Examiner's attention either to selected parts or away from other parts of these cited references. Any such markings were either present on the copies of these cited references when obtained by the associated individuals, or were made thereon during the study of these cited references by the associated individuals.

In accordance with 37 C.F.R. § 1.97(b), this information disclosure statement is being filed (i) within three months of the filing date of the above-identified patent application; (ii) within three months of the date upon which the above-identified

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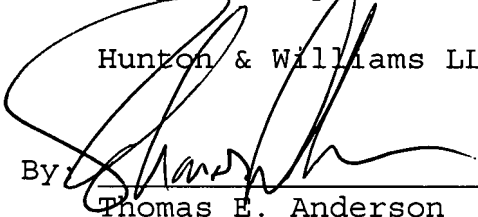
Client Reference No.: RA001.2003.1.C.US

patent application entered the national stage as set forth in 37 C.F.R. § 1.491; (iii) before the mailing date of a first Office Action on the merits for the above-identified patent application; or (iv) before the mailing date of a first Office Action after the filing of a request for continued examination under 37 C.F.R. § 1.114. Accordingly, no statement or fee is required.

Please charge any shortage in fees due in connection with the filing of this communication to Deposit Account No. 50-0206, and please credit any excess fees to such deposit account.

Respectfully submitted,

Hunton & Williams LLP

By: 
Thomas E. Anderson
Registration No. 37,063

TEA/asc

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Date: September 6, 2007



Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Application Number	10/716,595
Filing Date	November 20, 2003
First Named Inventor	Michael FARMWALD, et al.
Art Unit	2827
Examiner Name	Tan Nguyen
Attorney Docket Number	57941.000062

Sheet 1 of 5

U.S. PATENT DOCUMENTS

*Examiner Initials	Cite No.	DOCUMENT NUMBER Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1.	US- 4,623,986	11-18-1996	Chauvel	
	2.	US- 5,381,543	01-10-1995	Blomgren, et al.	
	3.	US- 5,359,722	10-25-1994	Chan, et al.	
	4.	US- 5,481,731	01-02-1996	Conary, et al.	
	5.	US- 5,666,322	09-09-1997	Conkle	
	6.	US- 4,907,199	03-06-1990	Dosaka, et al.	
	7.	US- 5,226,147	07-06-1993	Fujishima, et al.	
	8.	US- 5,111,386	05-05-1992	Fujishima, et al.	
	9.	US- 5,353,427	10-04-1994	Fujishima, et al.	
	10.	US- 5,268,639	12-07-1993	Gasbarro, et al.	
	11.	US- 4,712,190	12-08-1987	Guglielmi, et al.	
	12.	US- 5,214,610	05-25-1993	Houston	
	13.	US- 5,404,327	04-04-1995	Houston	
	14.	US- 5,021,985	06-04-1991	Hu, et al.	
	15.	US- 4,636,989	01-13-1987	Ikuzaki	
	16.	US- 4,727,363	02-23-1988	Ishii	
	17.	US- 5,479,635	12-26-1995	Kametani	
	18.	US- 4,866,675	09-12-1998	Kawashima	
	19.	US- 5,022,004	06-04-1991	Kurtze, et al.	
	20.	US- 5,278,974	01-11-1994	Lemmon	
	21.	US- 4,748,595	05-31-1988	Mattausch	
	22.	US- 4,876,670	10-24-1989	Nakabayashi, et al.	
	23.	US- 4,281,401	07-28-1981	Redwine, et al.	

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DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	10/716,595
		Filing Date	November 20, 2003
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		Art Unit	2827
		Examiner Name	Tan Nguyen
Sheet	2 of 5	Attorney Docket Number	57941.000062

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	24.	US- 4,943,944	07-24-1990	Sakui, et al.	
	25.	US- 4,984,217	01-08-1991	Sato	
	26.	US- 4,680,738	07-14-1987	Tam	
	27.	US- 4,839,856	06-13-1989	Tanaka	
	28.	US- 5,218,686	06-08-1993	Thayer	
	29.	US- 4,768,168	08-30-1988	Watanabe	
	30.	US- 4,677,648	06-30-1987	Zurfluh	
	31.	US- 4,752,915	06-21-1988	Suzuki, et al.	
	32.	US- 4,998,262	03-05-1991	Wiggers	
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		Examiner Name	Tan Nguyen
Sheet	3 of 5	Attorney Docket Number	57941.000062

FOREIGN PATENT DOCUMENTS

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		Art Unit	2827
		Examiner Name	Tan Nguyen
Sheet	4 of 5	Attorney Docket Number	57941.000062

OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS

*Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
	37.	Kumanoya, et al., "A reliable 1-Mbit DRAM with a Multi-Bit-Test Mode", IEEE Journal of Solid-State Circuits, Vol. SC-20, No. 5, pg. 909-913, October 1985.	<input type="checkbox"/>	<input type="checkbox"/>
	38.	Response to Decision SUA SPONTE Vacating Inter Partes Reexamination Filing Date, Filed in Inter Partes Reexamination Control No. 95/000,250; U.S. Patent No. 6,452,863, filed in the United States Patent and Trademark Office June 28, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	39.	Redacted Version of Request for Inter Partes Reexamination in Response to Decision SUA SPONTE Vacating Inter Partes Reexamination Filing Date Mailed June 15, 2007, Filed in Inter Partes Reexamination Control No. 95/000,250; U.S. Patent No. 6,452,863, filed in the United States Patent and Trademark Office on June 28, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	40.	Decision on Petition to Vacate Inter Partes Reexamination Filing Date, Issued in Reexamination Control No. 95/000,178; U.S. Patent No. 6,324,120, Mailed from the U.S. Patent and Trademark Office on August 14, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	41.	Petition to Vacate the Orders Granting Reexamination, filed in Control No.'s 95/000,166; U.S. Patent No. 6,426,916, 95/000,178; U.S. Patent No. 6,324,120, 95/000,183; U.S. Patent No. 6,182,184, filed in the United States Patent and Trademark Office on July 13, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	42.	Response to Decision on Petition to Vacate Inter Partes Reexamination Filing Date, Filed in Control No. 95/000,178; U.S. Patent No. 6,324,120, filed in the United States Patent and Trademark Office on August 22, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	43.	Transmittal, enclosing Third-Party Requester's Petition and Opposition to Petition to Vacate Orders Granting Reexamination, filed in Control No. 95/000,178; U.S. Patent No. 6,324,120, filed on July 26, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	44.	Transmittal, enclosing Third-Party Requester's Petition and Opposition to Petition to Vacate Orders Granting Reexamination, filed in Control No. 95/000,183; U.S. Patent No. 6,182,184, filed on July 26, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	45.	Decision on Petition to Vacate Inter Partes Reexamination Filing Date, Issued in Reexamination Control No. 95/000,183; U.S. Patent No. 6,182,184, Mailed from the U.S. Patent and Trademark Office on August 14, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	46.	Petition to Vacate the Orders Granting Reexamination, filed in Control No.'s 95/000,166; U.S. Patent No. 6,426,916, 95/000,178; U.S. Patent No. 6,324,120, 95/000,183; U.S. Patent No. 6,182,184, filed in the United States Patent and Trademark Office on July 13, 2007.	<input type="checkbox"/>	<input type="checkbox"/>

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		Art Unit 2827		
		Examiner Name Tan Nguyen		
Sheet	5 of 5	Attorney Docket Number 57941.000062		

OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS				
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			YES	NO
	47.	Response to Decision on Petition to Vacate Inter Partes Reexamination Filing Date, Filed in Control No. 95/000,183; U.S. Patent No. 6,182,184, filed in the United States Patent and Trademark Office on August 22, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
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	49.	Decision on Petition to Vacate Inter Partes Reexamination Filing Date, Issued in Reexamination Control No. 95/000,166; U.S. Patent No. 6,426,916, Mailed from the U.S. Patent and Trademark Office on August 14, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	50.	Petition to Vacate the Orders Granting Reexamination, filed in Control No.'s 95/000,166; U.S. Patent No. 6,426,916, 95/000,178; U.S. Patent No. 6,324,120, 95/000,183; U.S. Patent No. 6,182,184, filed in the United States Patent and Trademark Office on July 13, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	51.	Response to Decision on Petition to Vacate Inter Partes Reexamination Filing Date, Filed in Control No. 95/000,166; U.S. Patent No. 6,426,916, filed in the United States Patent and Trademark Office on August 22, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	52.	Kawamoto, et al., "A 288K CMOS Pseudostatic RAM", IEEE Journal of Solid State Circuits, Vol. Sc-19, No. 5, October 1984.	<input type="checkbox"/>	<input type="checkbox"/>
	53.	Hidaka, et al., "The Cache DRAM Architecture: A DRAM with an On-Chip Cache Memory", IEEE Micro, Vol. 10, No. 2, pg. 14-25, April 1990.	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
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